

Gp/3723  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

the application of:

SALDANA et al.

Application No. 09/748,708

Filed: December 22, 2000

For: POLISHING APPARATUS AND  
METHODS HAVING HIGH PROCESSING  
WORKLOAD FOR CONTROLLING POLISHING  
PRESSURE APPLIED BY POLISHING HEAD

L. Nelson  
#10/IDS  
Group Art Unit: 3723 8-30-02

Examiner: G. Nguyen

Atty. Docket No. LAM2P222A

Date: August 12, 2002

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on August 12, 2002.

Signed: \_\_\_\_\_

Kay Harlow

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, DC 20231

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to consider these references and to acknowledge such consideration by initialing the attached PTO Form 1449 at the appropriate locations.

A statement is hereby made that each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement. (37 CFR 1.97(e)(1)). A copy of an International Search Report is enclosed herein. If it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-0805 (Order No. LAM2P222A).

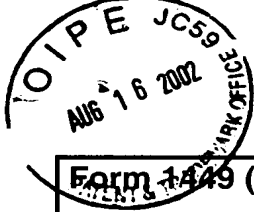
Respectfully submitted,  
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Form 1449 (Modified)

**Information Disclosure  
Statement By Applicant**

(Use Several Sheets if Necessary)

Attorney Docket No: U.S.  
LAM2P222A 09/748,708  
Applicant:  
Saldana et al.  
Filing Date: Group:  
December 22, 2000 3723

**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class
	A	5,631,839	05/1997	Lemoine	364	468.15
	B	5,899,798	05/1999	Trojan et al.	451	259
	C	6,139,400	10/2000	Sato et al.	451	10
	D					
	E					
	F					
	G					
	H					
	I					
	J					
	K					

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	L	0 055 782 A1	21/01/82	EPO	G05B	19/403		X
	M	0 331 747 A1	09/03/81	EPO	G05B	19/403		X
	N	GB 2 345 013 A1	28/06/00	UK	B24B	37/04		X
	O	11-31671	11/1999	JPO	B24B	37/04	X*	
	P							

\* Counterpart U.S. Patent is 6,139,400

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	Q	Weiderman and Kamenoff, "Hartstone Uniprocessor Benchmark: Definitions and Experiments for Real-Time Systems", p. 353-382, Journal of Real-Time Systems 4, (1992), The Netherlands.
	R	M. Levy, "At last: benchmarks you can believe", p. 59, 60, 62, 64, 66, 67, and 68, (PCT 230) Electrical Design News, (11/1998), Newton, MA,
	S	U. Schmid, "Monitoring Distributed Real-Time Systems", p. 33-56, 8359 Real-Time Systems, (7/1994), No. 1, Dordrecht, NE.
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.  
Include copy of this form with next communication to applicant.